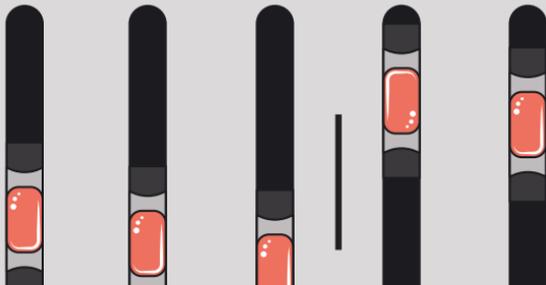


VERBOS ELECTRONICS

Voltage Multistage 10

outputs



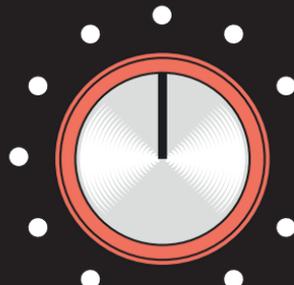
gate out



ref.



c.v. a



The Voltage Multistage was one of the five original modules we released in 2014. The Voltage Multistage 16 added several new features in addition to increasing the length. In the process of updating the original sized Voltage Multistage with these new features it became clear that we could also increase the length to 10 stages in the same 30HP. These extra 2 stages are helpful in various patches and a length of 10 stages is a nod to Mark's earliest CD4017 based sequencer designs back in the 90s.

The original Voltage Multistage has an analog sequencer core with several unique features that allow it to function as a sequencer with switchable slides on each step, multistage envelope of any length with sustain on any stage, any shape of LFO, voltage quantizer, and more. The Voltage Multistage 10 is no longer based on a CMOS counter core, but is built around discrete transistor stages, which enables gate inputs on each stage. This allows external gates to jump it to any stage, multiple loops and sequences that start at one stage and then enter into a loop later.

*The new sequencer core also knows when it has changed to a new stage. Therefore, when **strobe** is latched and a varying analog signal is changing the address, a gate (as well as **ref.** ramp and slide on **c.v. row a**) is sent out each time a new stage is selected rather than the gate staying high all the time as before. Now the **c.v. row a** switch positions are active when **strobe** is latched. This allows fluctuating random voltages to generate streams of notes completely free from the grid.*

*LEDs have been added to all outputs. All functions from the original VMS remain available. Patching stage gate outs into **enable**, **sustain** and **strobe** allows complex interactions at the same time as using stage select ins for unprecedented variability in an analog sequencer.*

30HP • 300g • +12v 68mA • -12v 28mA

stage gate outputs

master gate output

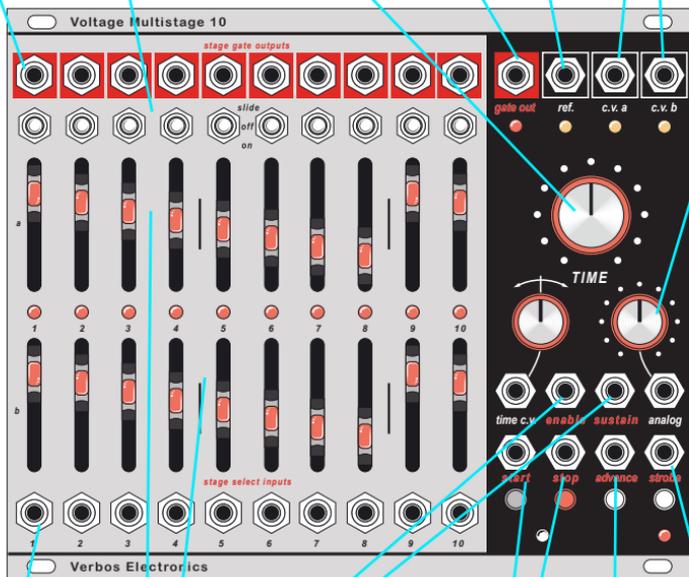
c.v. outputs

stage on/off/gate toggles

ref. ramp output

TIME control

analog stage select control



stage select gate inputs

enable/sustain inputs

advance

c.v. rows

start/stop

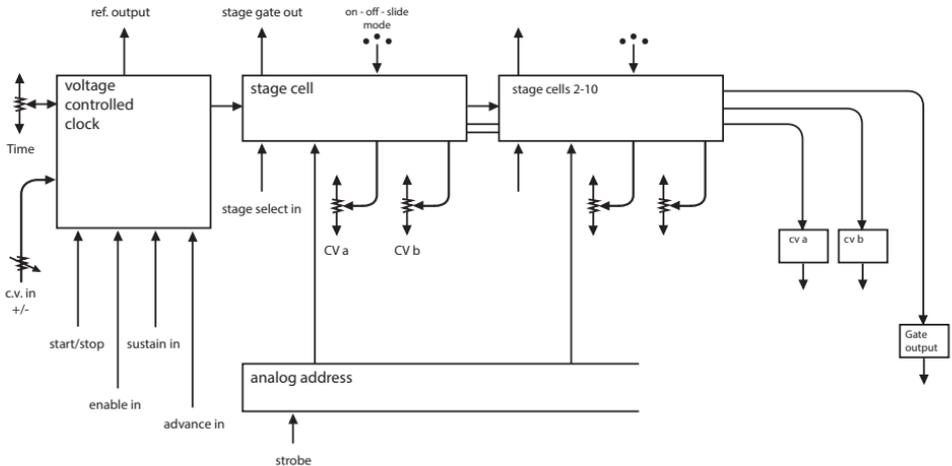
strobe input



VERBOS ELECTRONICS

designed and assembled in Berlin, Germany

Block Diagram



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